## ABSTRACT OF THE DISCLOSURE

A fuse structure for a semiconductor device on a substrate includes a fuse having an electrically conductive fuse line of a standard fuse length formed in an electrically conductive layer disposed over the substrate, and a pair of electrically conductive, inwardly bent interconnects formed in a first plurality of electrically conductive layers disposed over the substrate, below the electrically conductive layer in which the fuse line is formed. The inwardly bent interconnects couple the fuse line to a circuit area of the substrate disposed under the fuse line. The fuse structure may further include a protective guard ring formed around the fuse. The guard ring includes a second plurality of electrically conductive interconnects.